

IN THE CLAIMS

Please amend the claims as follows:

1. (Currently Amended) A semiconductor integrated circuit apparatus mounted on a predetermined circuit board, the apparatus comprising:

semiconductor information storage means for storing semiconductor information unique to the semiconductor integrated circuit apparatus[.,,]; and

semiconductor information output means connected to the semiconductor information storage means for (1) reading out the semiconductor information from the semiconductor information storage means in response to an externally a signal supplied signal by an external device that is external to, connectable to, and removable from the semiconductor integrated circuit apparatus, the external device storing an executable program and having a predetermined non-volatile storage region, and (2) ~~outputting~~ writing the read-out semiconductor information to the predetermined storage region of the external device,

wherein the semiconductor information output means includes

~~connection~~ first control means, which is configured to be connected to the external storage-means device storing ~~[[an]]~~ the executable program, for controlling a read-out operation of the program stored in the external ~~storage-means device~~, the program being used for executing the read-out operation of the semiconductor information, and

second control means for controlling the read-out operation and external outputting operation of the semiconductor information by executing the read-out program read by the ~~connection~~ first control means.

2-3. (Canceled)

4. (Previously Presented) The semiconductor integrated circuit apparatus according to claim 1, wherein

the semiconductor information storage means (1) stores an identification code as the semiconductor information, the identification code being assigned to allow identification of the semiconductor integrated circuit apparatus, and (2) outputs an electric signal according to the identification code in response to an input of a signal.

5. (Currently Amended) A circuit board on which a semiconductor integrated circuit apparatus is mounted, the circuit board comprising:

~~storage means;~~

semiconductor information storage means for storing semiconductor information unique to the semiconductor information circuit apparatus[[]]; and

semiconductor information output means, which is configured to be connected to the semiconductor information storage means, for (1) reading out the semiconductor information from the semiconductor information storage means in response to a signal supplied ~~from~~ outside by an external device that is external to, connectable to, and removable from the semiconductor integrated circuit apparatus, the external device storing an executable program and having a predetermined non-volatile storage region, and (2) writing the read-out semiconductor information ~~into the storage means~~ to the predetermined storage region of the external device,

wherein the ~~storage means~~ external device stores ~~[[an]]~~ the executable program being used for executing the read-out operation of the semiconductor information, and

wherein the semiconductor information output means controls (1) the read-out operation of the semiconductor information by executing the program read out from the

~~storage means~~ external device, and (2) the write-in operation of the semiconductor information to the ~~storage means~~ external device.

6. (Canceled)

7. (Currently Amended) An information readout method of reading out semiconductor information of a semiconductor integrated circuit apparatus mounted on a predetermined circuit board, the method comprising:

writing an executable program, which is for reading out semiconductor information unique to the semiconductor integrated circuit apparatus and stored in the semiconductor integrated circuit apparatus, into a predetermined external ~~storage means~~ device that is external to, connectable to, and removable from the semiconductor integrated circuit apparatus, the external device storing an executable program and having a predetermined non-volatile storage region;[[,]]

reading the executable program written into the external ~~storage means~~ device and reading out the semiconductor information by executing the read program[[,]]; and

writing the read-out semiconductor information into [[a]] the predetermined region of the external ~~storage means~~ device.

8. (Currently Amended) A semiconductor integrated circuit apparatus mounted on a predetermined circuit board, the apparatus comprising:

a semiconductor information storage section configured to store semiconductor information unique to the semiconductor integrated circuit apparatus, and

a semiconductor information output section connected to the semiconductor

information storage section and configured (1) to read out the semiconductor information from the semiconductor information storage section in response to a signal supplied ~~from outside~~ by an external device that is external to, connectable to, and removable from the semiconductor circuit apparatus, the external device storing an executable program and having a predetermined non-volatile storage region, and (2) to output the read-out semiconductor information ~~to the outside~~ to the predetermined storage region of the external device,

wherein the semiconductor information output section includes

~~connection~~ first control means, which is configured to be connected to the external ~~storage means~~ device storing ~~[[an]]~~ the executable program, for controlling a read-out operation of the program stored in the external ~~storage means~~ device, the program being used for executing the read-out operation of the semiconductor information, and

second control means for controlling the read-out operation and external outputting operation of the semiconductor information by executing the read-out program read by the ~~connection~~ first control means.

9. (Previously Presented) The semiconductor integrated circuit apparatus of Claim 7, wherein the semiconductor information includes at least one of a wafer number, information of a position on a wafer, and a manufacture time of the semiconductor integrated circuit.

10. (New) The semiconductor integrated circuit apparatus of Claim 1, wherein the external device is one of a flash memory device and a compact disc.